

GP 2731 AYC  
2/23IN THE UNITED STATES PATENT AND TRADEMARK OFFICE **RECEIVED**

In re Application of: Buckland, et al. FEB 17 2000  
 Serial No.: 09/414,082 GROUP 2700  
 Filing Date: October 6, 1999  
 Title: METHOD FOR USING A PRE-CONFIGURED TDM  
 SWITCH AND TRAFFIC DISCARD TO  
 FACILITATE UPSR SELECTION



Honorable Assistant Commissioner  
 for Patents  
 Washington, D.C. 20231

Dear Sir:

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on

February 7, 2000

  
 Name

February 7, 2000

Date of Signature

**INFORMATION DISCLOSURE STATEMENT**

Applicants respectfully request, pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98, that the references listed on the attached PTO-1449 form be considered and cited in the examination of the above-identified patent application. A copy of these references are enclosed for the convenience of the Examiner. Furthermore, pursuant to 37 C.F.R. § 1.97(h), no representation is made that these references qualify as prior art or that these references are material to the patentability of the present application.

Applicants therefore respectfully submit that the claims of Applicants' above-referenced patent application are patentably distinguishable from these references.

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Pursuant to 37 C.F.R. § 1.97(b), Applicant's believe that this Information Disclosure Statement has been filed before the mailing date of the First Office Action in this case. Therefore, Applicants believe that no fee is due. However, if a fee is required, the Commissioner is hereby authorized to charge any fees or credit any overpayments to Deposit Account No. 02-0384 of Baker Botts L.L.P.

Respectfully submitted,

BAKER BOTTS L.L.P.  
Attorneys for Applicants

Barton E. Showalter  
Reg. No. 38,302

2001 Ross Avenue  
Dallas, Texas 75201-2980  
(214) 953-6509

Date: February 7, 2000

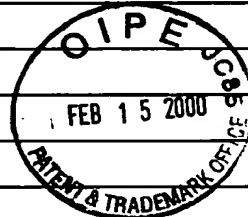
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PTO-1449 <b>Information Disclosure Citation in an Application</b>	Application No. 09/414,082	Applicant(s) Buckland, et al.
	Docket Number 062891.0340	Group Art Unit Filing Date Oct. 6, 1999

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GROUP 320

## U.S. PATENT DOCUMENTS

	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
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## FOREIGN PATENT DOCUMENTS

	DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
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P							
Q							
R							

## NON-PATENT DOCUMENTS

		DOCUMENT (Including Author, Title, Source, and Pertinent Pages)	DATE
<i>JMP</i>	S	"Transport Systems Generic Requirements (TSGR): Common Requirements," (A Module of FR-440, TSGR), Generic Requirements GR-499-CORE, Issue 2, December, 1998, Bellcore.	12/1998
<i>JMP</i>	T	"Synchronous Optical Network (SONET) Transport Systems: Common Generic Criteria," A Module of TSGR, FR-440, Generic Requirements GR-253-CORE, Issue 2, December, 1995, Bellcore.	12/1995
<i>JMP</i>	U	"Digital Interface Between the SLC®96 Digital Loop Carrier System And A Local Digital Switch," Technical Reference TR-TSY-000008, Issue 2, August, 1987, Revision 1, September, 1993, Bellcore.	09/1993

EXAMINER	DATE CONSIDERED
<i>Jack M. Peltz</i>	10-21-2002

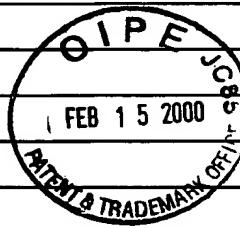
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.

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<b>PTO-1449</b> <b>Information Disclosure Citation</b> <b>in an Application</b>	Application No.	Applicant(s)
	09/414,082	Buckland, et al.
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	DOCUMENT (Including Author, Title, Source, and Pertinent Pages)	DATE
S	"Integrated Digital Loop Carrier System Generic Requirements, Objectives, and Interface," (A module of TSGR, FR-440), Generic Requirements GR-303-CORE, Issue 2, Bellcore, December, 1998.	12/1998
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U		

EXAMINER	DATE CONSIDERED
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